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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/823,455	04/13/2004	Vladislav Vashchenko	NSC1-M4700 [P05849]	4718
28584	7590	01/09/2006	EXAMINER	
STALLMAN & POLLOCK LLP 353 SACRAMENTO STREET SUITE 2200 SAN FRANCISCO, CA 94111			NGUYEN, LONG T	
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 01/09/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/823,455	Applicant(s) VASHCHENKO ET AL.	
	Examiner Long Nguyen	Art Unit 2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 26 October 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1 is/are allowed.
- 6) ☒ Claim(s) 2 and 4 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 26 October 2005 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

1. The drawings filed on 10/26/05 are objected to because arrows of the bodies of transistors 104 (Figure 1A) should be pointed in instead of point-out as currently shown. Note that, the arrow of the body of the NMOS transistor must be pointed in.

The drawings are also objected to because the unit of the capacitor is not correctly provided in Figures 1C, 1D, 2A, 2B and 4. For example, in those Figures, "C=100f" should be changed to --C=100fF--, "C = 1p" should be changed to --C = 1pF--, "C = 10p" should be changed to --C = 10pF--.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

2. The disclosure is objected to because of the following informalities: on page 2, line 22, "100f" should be changed to --100fF--. Appropriate correction is required.

Claim Objections

3. Claims 3 and 4 are objected to because of the following informalities:

Claim 3, line 2, "at" should be changed to --of--.

Claims 3 and 4, line 2, "100f" should be changed to --100fF--

Appropriate correction is required.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claim 2 is rejected under 35 U.S.C. 102(b) as being anticipated by Thomson et al. (USP 5,721,502).

With respect to claim 2, Figure 4 of the Thomson et al. discloses a circuit, which includes: a resistive divider (R1, R2) comprising a first resistor (R2) and a second resistor (R1); a positive voltage supply (Vdd) and a negative voltage supply (V⁻); an NMOS transistor (28) having its drain (38) connected to the positive voltage supply (by way of 26 or R3), its source (40) connected to the negative voltage supply (V⁻), and its gate (32) connected to the resistive divider common node; and a capacitor structure (C1) disposed between the source of the NMOS transistor (28) and the positive supply voltage (Vdd).

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Thomson et al. (USP 5,721,502).

With respect to claim 4, Figure 4 of the Thomson et al. reference meets all the limitations of this claim except for the capacitance value of capacitor C1 is 100fF. However, it is seen that the specific limitation drawn to a particular capacitance value of the capacitor C1 is seen as design expedient depending on a desired result. Therefore, it would have been obvious to one having skill in the art at the time the invention was made to modify the circuit in Figure 4 of Thomson et al. by utilize the capacitance of 100fF for the capacitor C1 for the purpose of achieving a desired design optimization such as a desired delay and/or operation's speed of the circuitry. Thus, this modification meets all the limitations of claim 4.

Allowable Subject Matter

8. Claims 1 and 3 would be allowed if amended to overcome the above informalities (for claim 3).

Response to Arguments

9. Applicant's arguments filed on 10/26/05 have been fully considered but they are not persuasive.

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Applicant argues that Figure 4 of Thomson et al. does not show capacitor C1 disposed between the source of the NMOS transistor and the positive supply voltage. However, this argument is not persuasive because, in Figure 4 of Thomson et al., capacitor C1 is clearly disposed between the source of the NMOS transistor (node 40 of transistor 28) and the positive power supply voltage (Vdd, by way of resistor R2) which meets the claim's language "a capacitor structure disposed between the source of the NMOS transistor and the positive supply voltage".

Conclusion

10. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directly to Examiner Long Nguyen whose telephone number is (571) 272-1753. The Examiner can normally be reached on Monday to Thursday from 8:00am to 6:30pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tim Callahan, can be reached at (571) 272-1740. The fax number for this group is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



1/31/06

LONG NGUYEN
PRIMARY EXAMINER